

Bi-Directional GaN & IsoFast Revolutionize Power Conversion with Single-Stage BDS Converters, Shrinking Size, Cost & Complexity

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Over 70% of today's high-voltage power converters use a 'two-stage' silicon topology. For example, a typical AC-DC EV On-Board Charger (OBC) implements an initial power-factor-correction (PFC) stage and a follow-on DC-DC stage, which is buffered with bulky 'DC-link' capacitors.

The problem with this topology is that the resulting systems are large, lossy, complex and expensive. In this article we look at a new class of power converter that enables single-stage conversion and allows bi-directional power to move back and forth between storage and the grid as required.

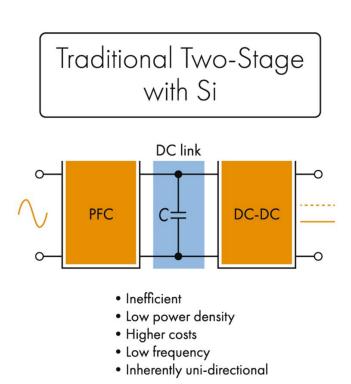


Figure 1: Over 70% of today's high-voltage power converters use a two-stage topology, with a PFC stage followed by a DC-DC stage to deliver the desired voltage

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A short history of bi-directional power converters

An ideal switch is bi-directional, able to block voltage in both directions AND handle current in both directions. It would offer minimal conduction and dynamic losses, dissipate heat effectively, and have a high-power density.

In the nearly 80 years since the creation of the bipolar transistor (1947), there have been many developments in power semiconductors that have moved the industry closer to this ideal.

The thyristor (1957) was capable of handling voltage in both directions, but not current. The triac (1958) could handle both current and voltage, but was very slow and operated at just 50 or 60 Hz, matching the AC line.

The MOSFET (1959) could operate at tens of kilohertz, even 100 kHz. A bi-directional MOSFET was subsequently created, but this was low power and had limited applications. The silicon IGBT (1980) followed and offered much higher power, but bi-directional functionality remained a challenge, with the IGBT able to handle either current OR voltage, but not both in a single device.

The introduction of wide bandgap semiconductors enabled significant gains in power technology, significantly improving power density; but again, these initial devices lacked bi-directional capability. The use gallium nitride (GaN) took things further, providing higher switching frequencies and high power, with integrated control and protection on the same chip. However, it still wasn't bi-directional.

Last month, however, Navitas launched the industry's first bi-directional 650V GaN power IC, enabling the transition of power conversion from a two-stage, to single-stage topologies.

Navitas's NV6427 and NV6428 are bi-directional GaN power ICs that deliver at 650 V_{SS} continuous with an $R_{SS(ON)typ.}$ of $50m\Omega$ (I_{SS} of 49 A) and $R_{SS(ON)typ.}$ of $100m\Omega$ (I_{SS} of 25 A). They have zero reverse-recovery charge and operate at up to 2 MHz. They are housed in a TOLT-16L thermally-enhanced top-cooled package.

Navitas' bi-directional GaN power switch

The advent of bi-directional GaN power switch consolidates the two stages into a single, high-speed, high-efficiency stage, and in the process, eliminating the bulky capacitors and input inductors. It is able to handle current and voltage in both directions and switch at very high frequency and is ideally designed for single-stage converters.

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Basic GaN Bi-Directional FET

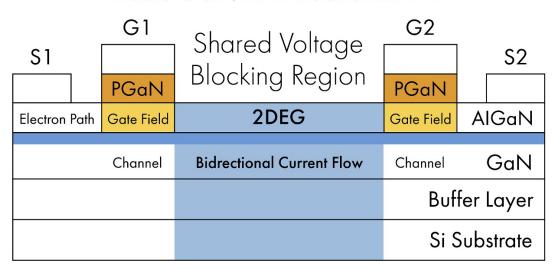


Figure 2: Bi-directional GaN switches use a GaN/AlGaN structure on silicon substrate, to create a two-dimensional electron gas (2DEG) conducting channel with two power terminals and two gates

The bi-directional GaN must handle voltage in both directions, and therefore requires separate gates to control current flow depending on polarity. To achieve this, an GaN/AlGaN epi-stack is grown on a silicon substrate to form a 2DEG conducting channel. The processed device structure features two power terminals and two gates.

Implementing this structure alone would have performance issues as the silicon substrate is not associated with the source terminals and therefore floats, leading to a substrate potential build-up and reducing the 2DEG concentration due to this 'back-gating' effect.

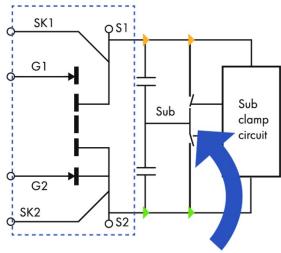
Navitas was the first to develop and release an active-substrate-clamp to autonomously drive the silicon substrate to the source with the lowest potential. This has the resulting benefit of enabling the BDS to operate stably with no shifting in resistance and to run 15°C cooler in many applications than alternative unclamped solutions.

This improvement can be seen in figure 3, with current (green triangle wave), and voltage across the switch (clamped – pink and unclamped – white curve). Here we see proper clamping removes any increases, improving efficiency and enabling a smoother operation.

CARRON NEUTRAL Navitas



Integrated Substrate Clamp



Automatically detects and connects alternative Sources to substrate for highest performance, efficiency & reliability.

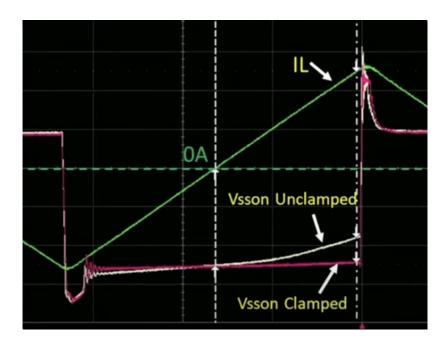


Figure 3: Current (green) and clamped (pink) / unclamped (white) voltage across the switch – with clamped voltage not displaying a efficiency-reducing voltage spike

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It should also be noted that bi-directional GaN also requires a dedicated driver to control its two gates, which must be capable of handling high transient conditions, very high voltage isolation, and ensure exceptional signal integrity. In this case, the device is able to operate at over 5 kV, and handles extreme transients up to 200 V/ns.

Therefore, to complement the bi-directional GaN, the IsoFast™ driver has been developed as a high-speed isolated GaN driver, specifically tailored for this switch, operating at over 1 MHz and handling 5 kV while maintaining high-speed signals with great integrity.

Benefits of the single-stage topology

As we said at the start, the overwhelming majority of power converters use a two-stage topology, which is slower as well as being less efficient, and results in bulky power converters with losses that are far from ideal.

As an industry, we've made great advances, especially with the introduction of silicon carbide (SiC) and GaN to improve these two-stage converters and improved efficiency, power density and switching speed. But we're approaching the limits of this topology, even with wide bandgap materials.

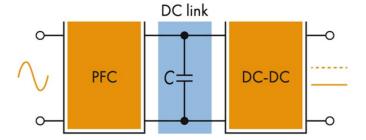
The creation of single-stage BDS converters not only eliminates the PFC stage, but with it, the electrolytic capacitors and DC link capacitors. Furthermore, the topology is inherently soft switching, making it possible to exploit the high-frequency advantage while also significantly shrinking passive components. As a result, this delivers a 30% improvement in power density, a 10% improvement in energy savings, and a 10% cost reduction.

Arguably, more important however is the ability to deliver the bi-directional energy flow, which is crucial for allowing renewables, the AC power grid, energy storage (including electric vehicles) to efficiently exchange power.





New Single-Stage with Bi-Directional GaN



- 20-30% energy savings
- 20-30% higher density
- 0-20% more expensive
- Medium frequencies
- Inherently uni-directional

Real-world example: solar microinverter

Moving away from theoretical values, let's look at a traditional 400 W solar microinverter with a two-stage topology, which transfers from the solar panel to storage or the grid.

This uses a DC-DC step-up transformer, followed by a 400 V_{DC} bus, which then converts 400 V_{DC} to AC in order to feed the grid. As we can see in figure 4, this design requires magnetic components, plus bulk capacitors, and multiple switching components.

Figure 4b shows a single-stage BDS that is being implemented by a leading solar microinverter manufacturer. For this, the design delivers a more powerful design (500 W) in a significantly smaller form factor, eliminating one magnetic component, and reducing the component count. This topology increases system efficiencies from 96% to 97.5% and reduces generation costs by 30%, from \$0.10/W to \$0.07/W.

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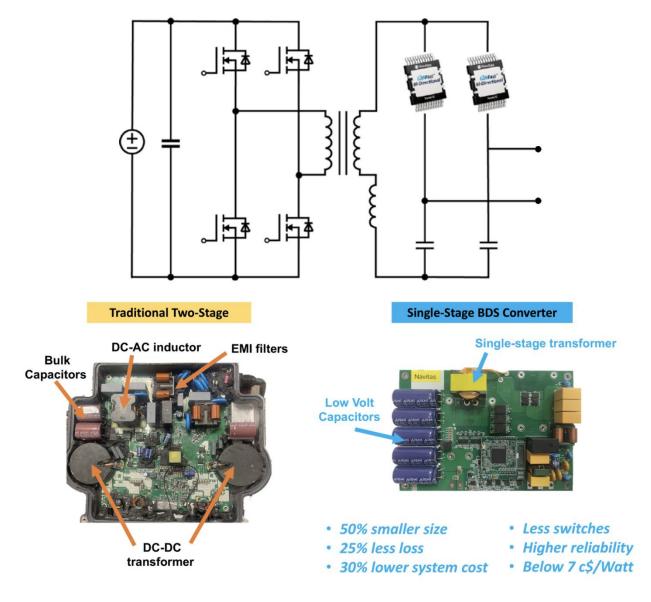


Figure 4a/b: Circuit diagram for a 500 W solar microinverter in a cyclo-converter-based topology, with traditional 400 W 2-stage solution (left) and single-stage BDS converter solution showing reduction in size, component count, and complexity (right)

For further information on bi-directional GaNFast Bi-Directional and IsoFast ICs, please visit https://navitassemi.com/bi-directional-gan/ or contact info@navitassemi.com/bi-directional-gan/ or contact info@navitassemi.com/bi-directional-gan/ or contact info@navitassemi.com/bi-directional-gan/ or contact info@navitassemi.com/bi-directional-gan/ or contact info@navitassemi.com/bi-directional-gan/ or contact info@navitassemi.com/bi-directional-gan/ or contact info@navitassemi.com/bi-directional-gan/ or contact info@navitassemi.com/ or contact info@navitassemi.com

