SEMICON<sup>®</sup> TAIWAN Sep. 6-8, 2023, TaiNEX 1&2, Taipei

GàNSafe<sup>™</sup> Power ICs Create Highest Density and Efficiency in Data Center & EV Power Systems

#### Charles Bailley Sr. Director Navitas Semiconductor

Taipei, September 2023

ir@navitassemi.com

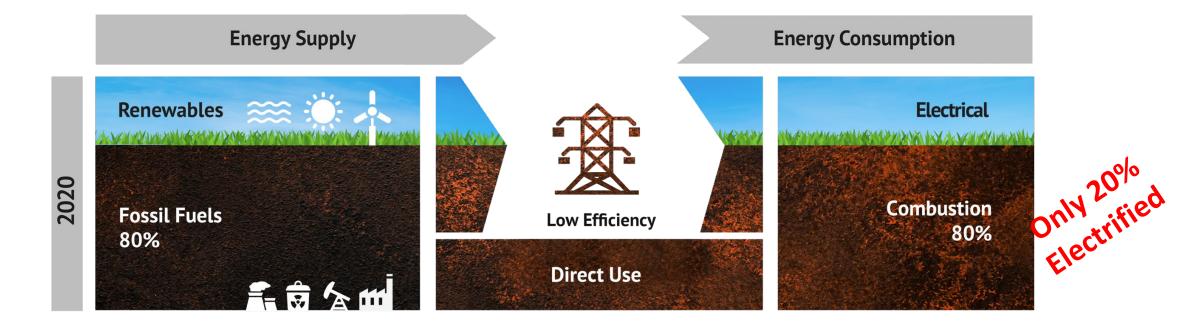
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# Navitas Electrify Our World™



### **The Fossil Fuel Challenge**



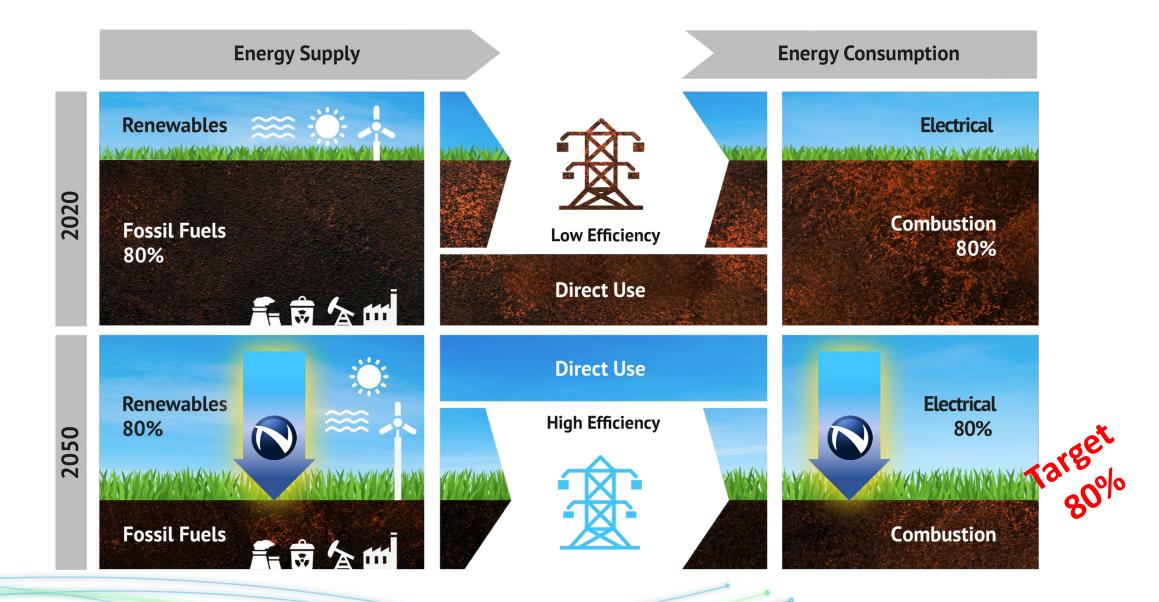


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### **Electrify Our World**<sup>™</sup>





### **Accelerating Sustainability**





February '22 First GaN sustainability report based on global standards.

#### Every GaNFast<sup>™</sup> IC

saves

4 kg CO<sub>2</sub>



**4x-10x** lower component CO<sub>2</sub> footprint than silicon

**28% lower** lifetime CO<sub>2</sub> footprint for chargers / adapters

Accelerates transition from ICE to EV by **3 years**, saving **20%/yr** of road-sector emissions by 2050

GaN + SiC save up to 6 Gton / year by 2050



May '22 World's first semiconductor company CarbonNeutral® certified



August '22 First 100,000 tons CO<sub>2</sub> saved (Over 170,000 as of August 2023)



'Best Sustainability' - finalist 'Best Climate-Related (Mid-Cap)' - runner up



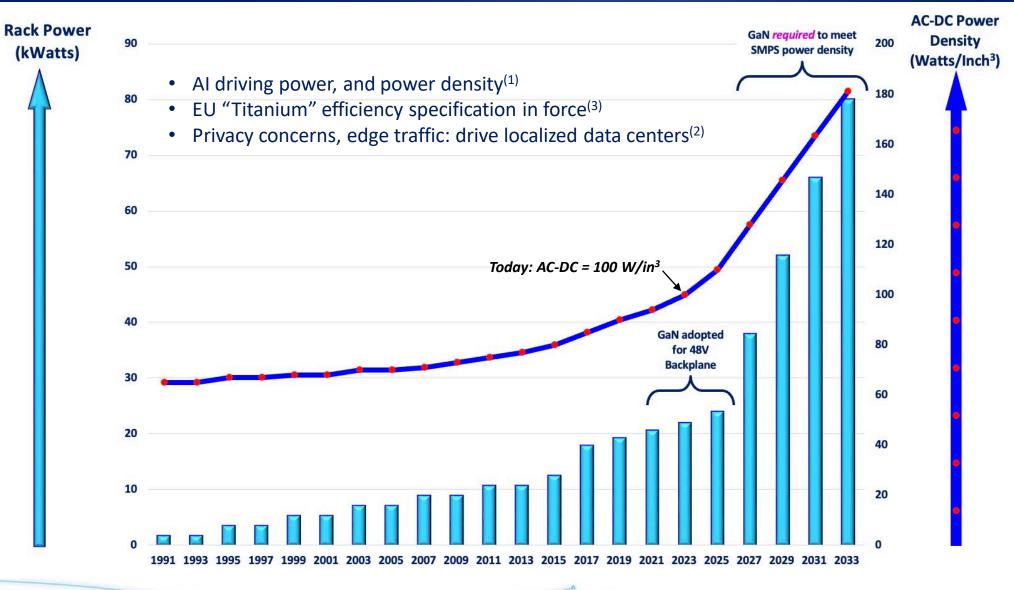
October '22 Recognized for industry-leading sustainability reporting

### **Accelerating Data Center Power, Efficiency**









1. Cerebras white paper / website

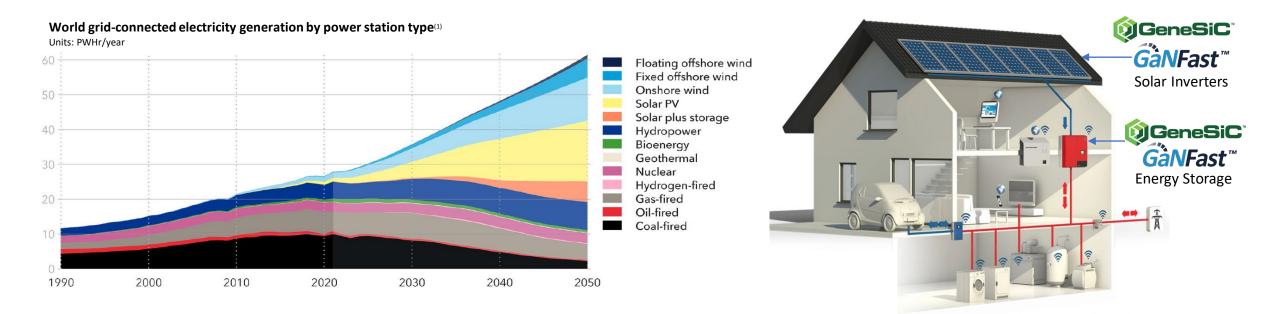
2. TD Cowen, per "Al to drive data center investments", LightReading.com, 4-26-23

3. European Union 'Directive 2009/125/EC, 2019 Annex', power supplies must be >96% efficiency peak, as of 1-1-23

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## **Accelerating Energy Demand: Solar/ESS**

#### Navitas



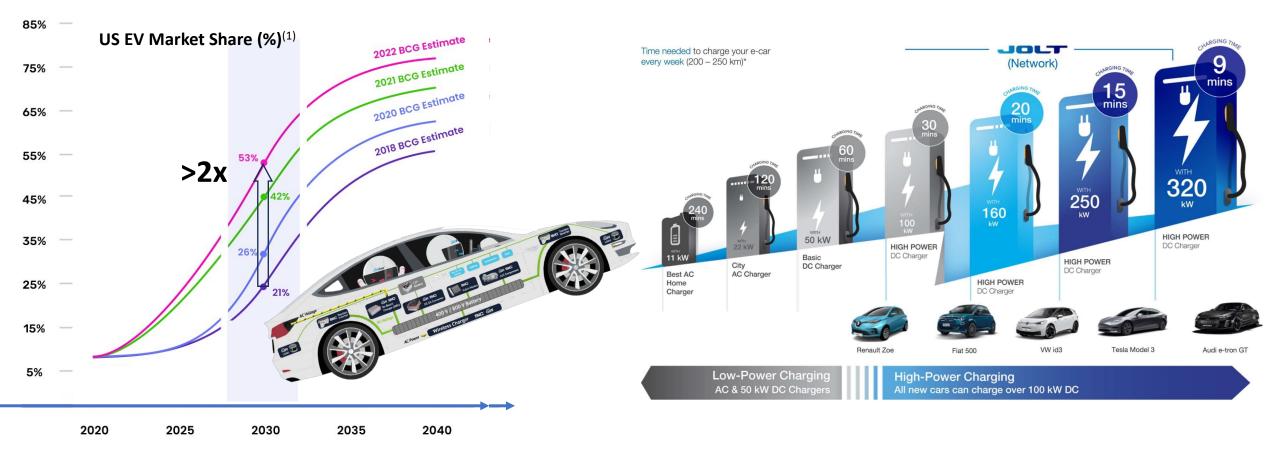
- Global electricity supply +2.3x by 2050<sup>(1)</sup>
- Solar / energy-storage systems (ESS) up from ~2% to 38%<sup>(2)</sup>
- Energy storage critical to balance supply / demand

1. DNV Energy Transition Outlook 2022, updated January 2023. Grid-connected energy supply estimated to increase from 27 to 62 PWH/year.

2. DNV: Solar/ESS grows to 38% of supply by 2050. Historical data per IEA WEB (2022), GlobalData (2022)



## Accelerating EV: Fast Charging (OBC, Roadside) 🔊 Navitas

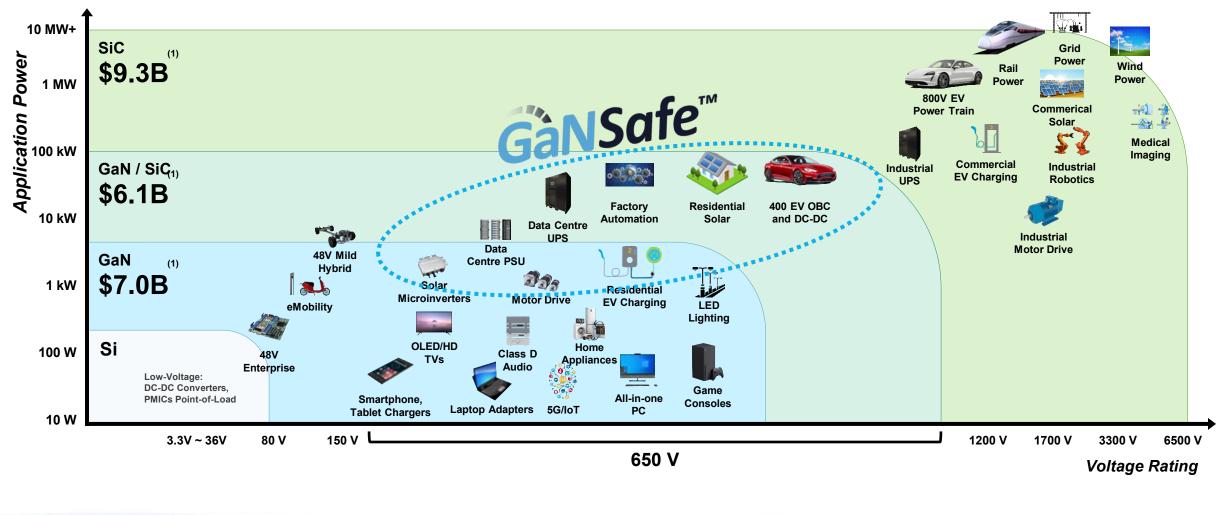


1. Chart BCG, via https://www.recurrentauto.com/research/ev-adoption-us

2. https://jolt.energy/whats-the-difference-between-ac-dc-and-ultra-fast-charging/

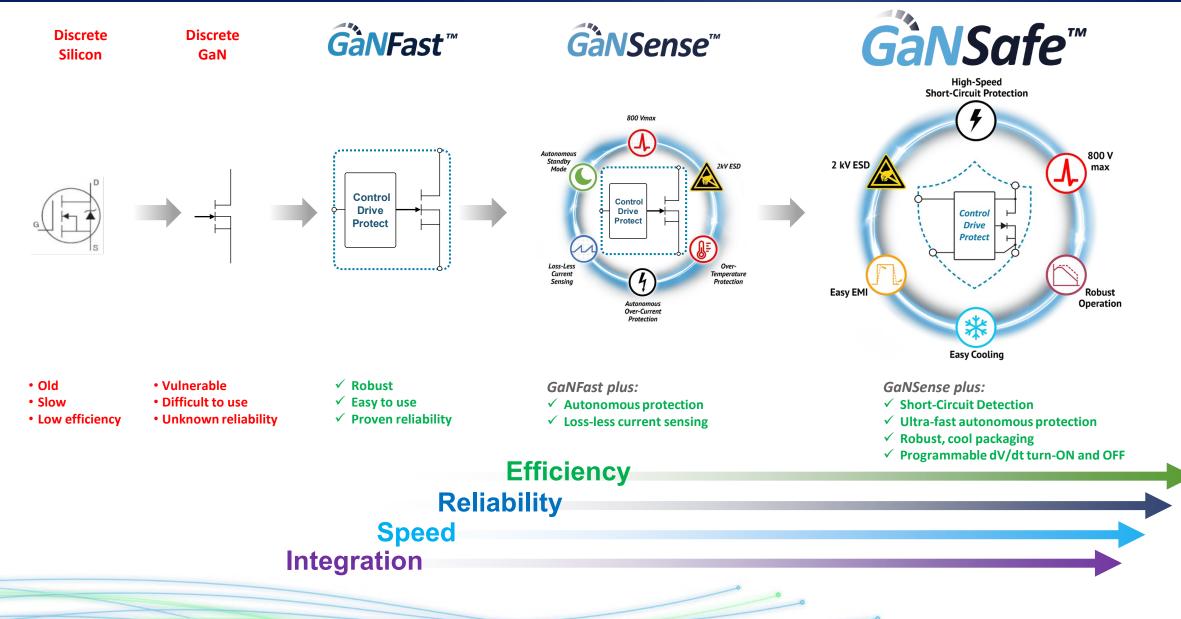
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# **Gansafe**<sup>TM</sup>: Accelerating GaN into High Power Navitas



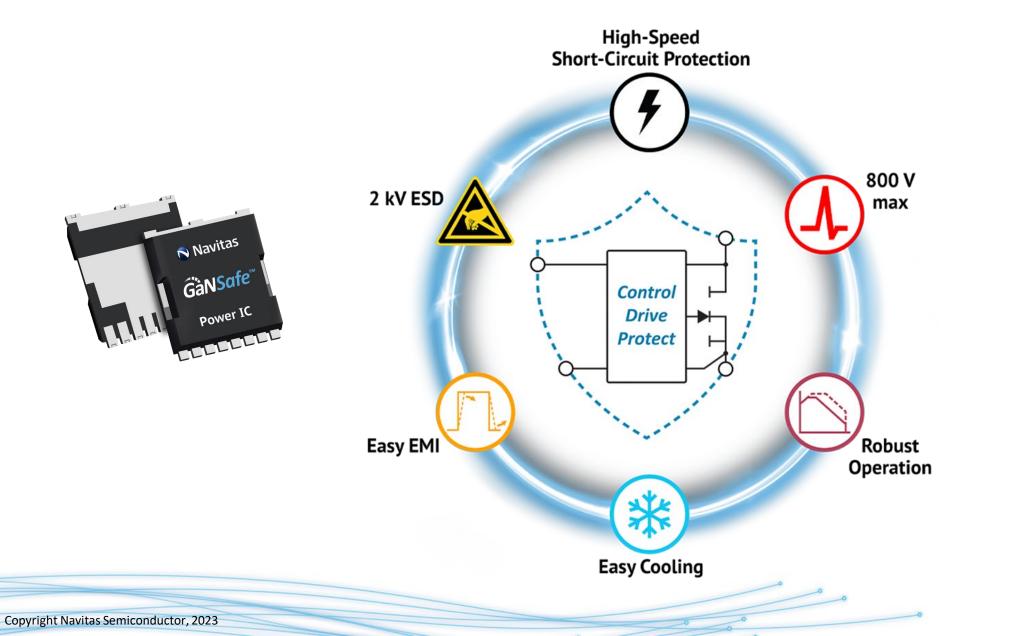
1. Navitas company estimates, potential market opportunity in 2026 is \$22B+ for GaN and SiC, replacing certain of the silicon market share. Axes not to scale

# **GaNSafe**<sup>TM</sup>: Ultimate Performance, Reliability Navitas

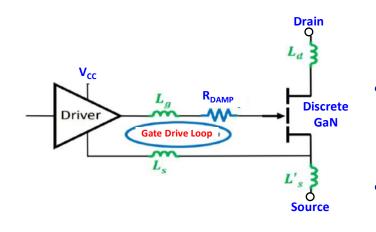


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### **GaNSafe**<sup>TM</sup>: World's Safest GaN Power Semiconductor

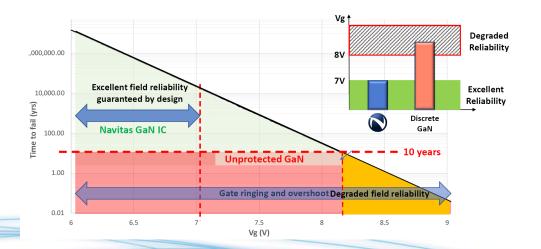


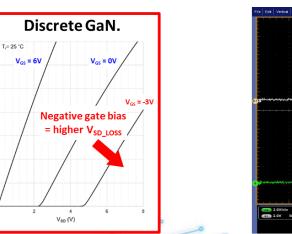
# **GaNFast**<sup>™</sup> to **GaNSafe<sup>™</sup>**: Max 4<sup>th</sup>-Gen Reliability Navitas



- Discrete GaN = high risk
  - Weak gate, high loop inductance
  - Shoot-thru risk multiplied by increased di/dt in high-power applications
- GaNFast<sup>™</sup> integrated, regulated gate drive, <u>zero</u> loop inductance
- Fewer components, smaller PCB,
- Higher efficiency, lower system cost
- GaNSafe<sup>™</sup> optimized for high power
- More protection (300 ns Desat SCP, OTP, UVLO, ESD, etc.)
- More control (dV/dt ON & OFF, etc.)
- Industry-standard, robust, cool packaging

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# **GaNSafe**<sup>™</sup> Delivers High-Power Reliability

#### Double-pulse test: 400 V, 30 A, R<sub>SERIES</sub> = 11 mΩ



**Discrete GaN** 42 mΩ max

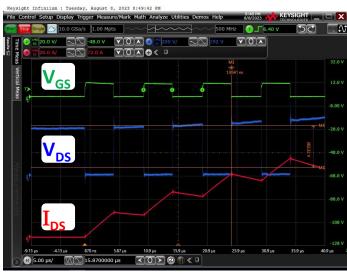
#### Significant spikes Excessive turn-ON ringing 250 V undershoot

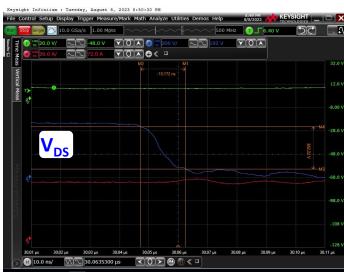


30.001 µs

∑30.0108600 μs <

30.006 µs





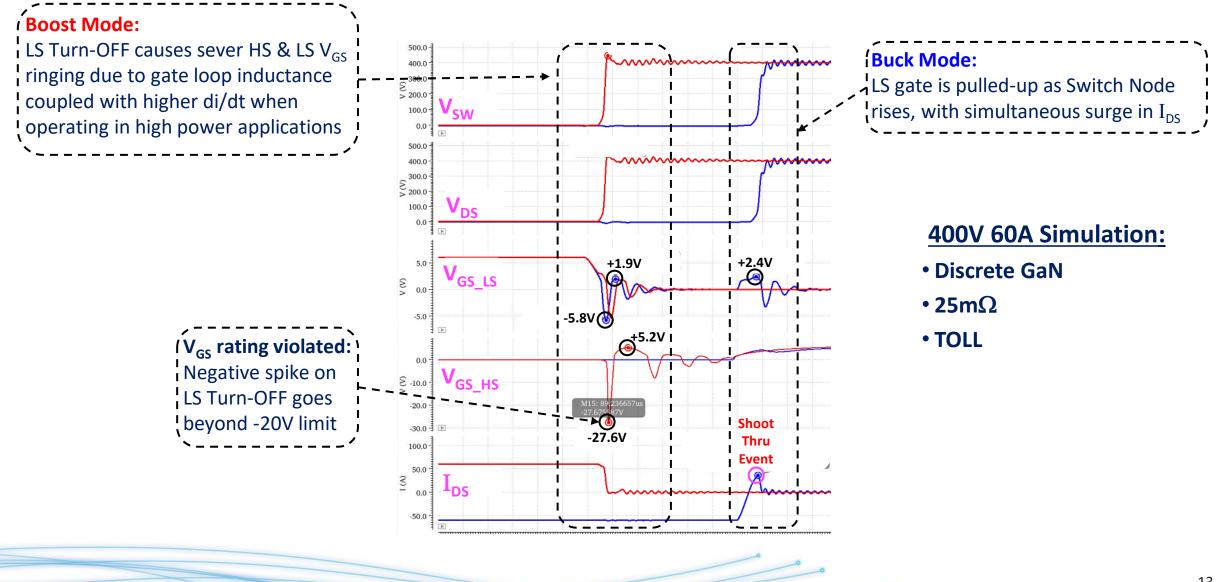


Navitas

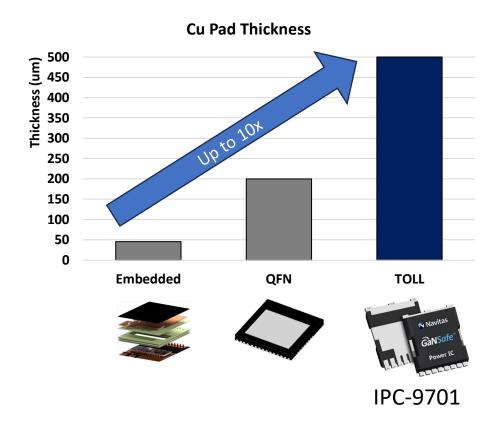
45 mΩ max (NV6513)

No voltage spikes No ringing No undershoot

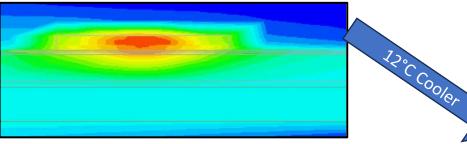
### **Discrete GaN: Major Challenge in High Power** Navitas



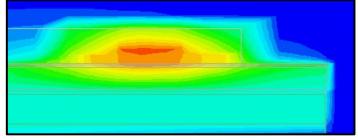
# **GaNSafe**<sup>TOLL</sup>: Optimal System-Level Cooling Navitas



QFN: T<sub>j</sub> = **112°C** 



TOLL:  $T_j = 100^{\circ}C$ 

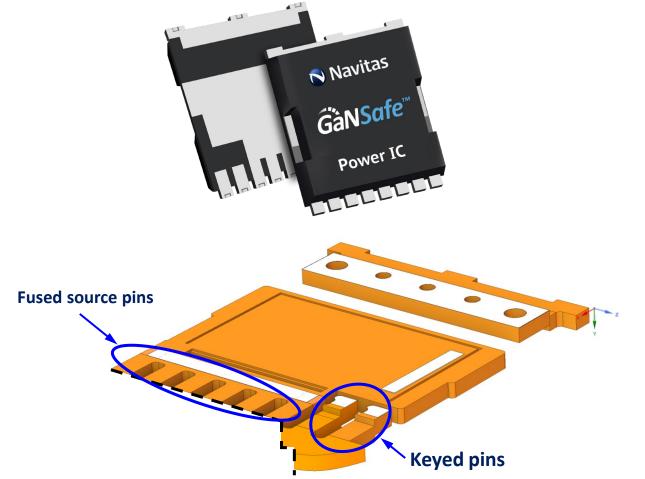


**GàNSafe**<sup>™</sup>

- Optimized, system-level cooling ( $R_{\Theta_{J-A}}$ )
  - •Larger, thicker Cu pad
  - •High-conductivity die attach

1. Navitas' simulated temperature gradients for TOLL and QFN under identical system thermal design and 200 LFM airflow

# GaNSafe<sup>™</sup> TOLL: Robust, Reliable Packaging



L. IPC-9701 "Thermal Cycling Test Method for Fatigue Life Characterization of Surface Mount Attachments"

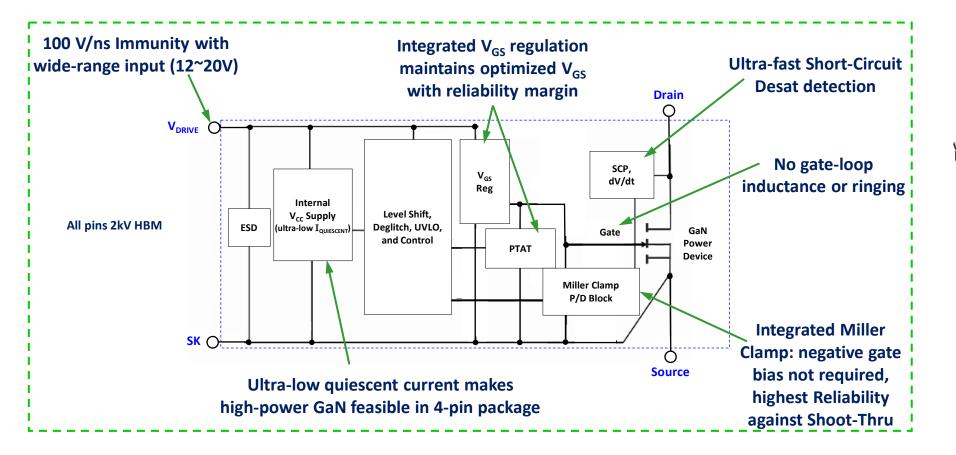
- TOLL = "Transistor Outline Lead-Less"
- 10 x 10 mm
- Mechanically robust, novel leadframe
  - Keyed  $V_{\text{DRIVE}}$  and SK pins
    - Improved mechanical performance
  - Fused source pins
    - Improved thermals)
  - Passed IPC-9701 for long mechanical lifetime



# **GàNSafe**™

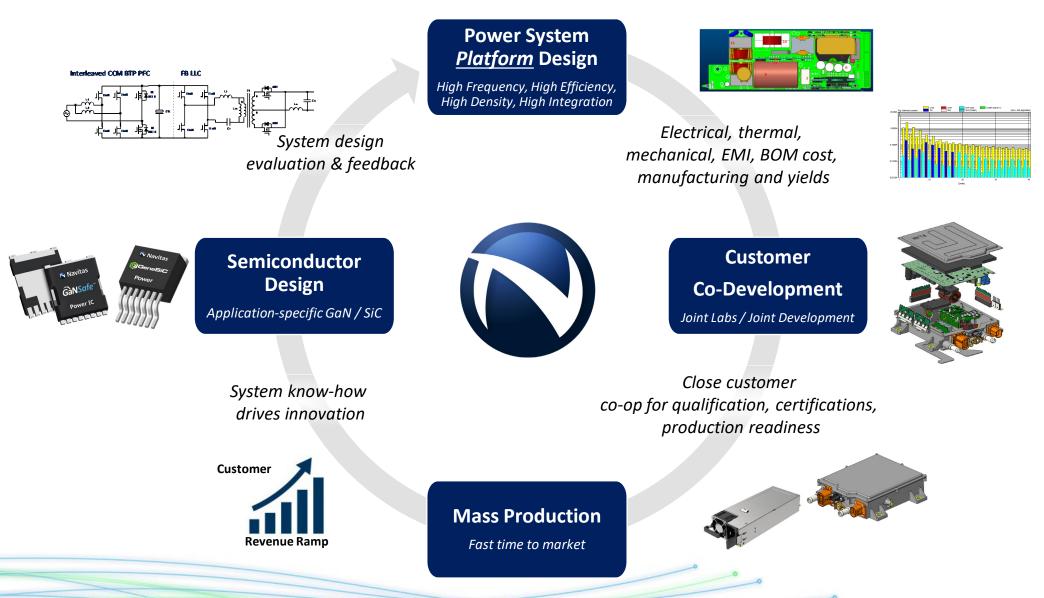
Integrated GaN gate drive, protection and features, in 4-pin industry-standard thermally-enhanced package...

# **Gansafe**<sup>TM</sup>: Reliable High-Power Gan in 4 Pins Navitas



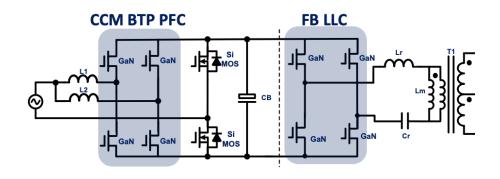


### **Accelerating TTM Requires Platform Focus**



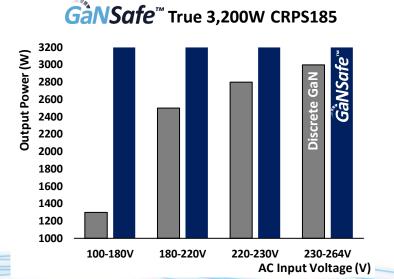
# **GaNSafe**<sup>TM</sup>: Max Performance for Data Center Navitas

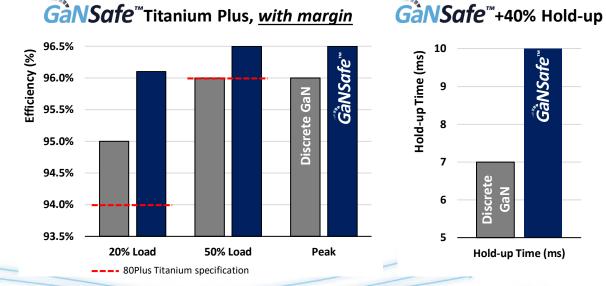
- Data center AC-DC 'silver box' (12V)
- CRPS185 form factor



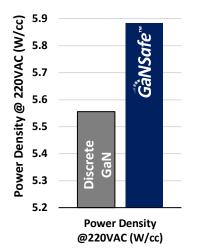




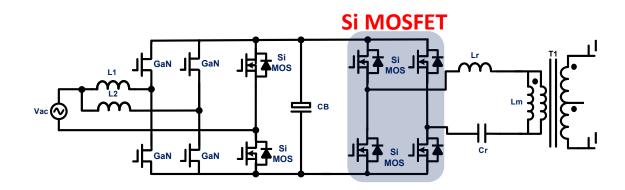


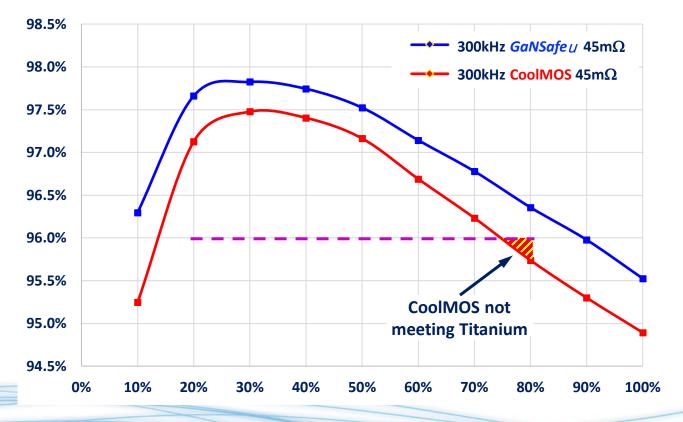


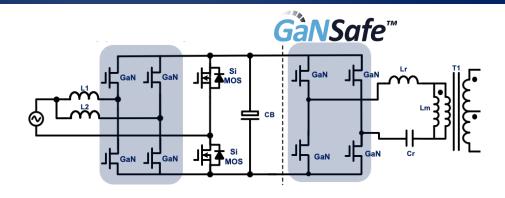
GaNSafe<sup>™</sup>~100W/in<sup>3</sup>



# **GaNSafe**<sup>™</sup>: Higher Efficiency than Si in LLC







- GaNSafe<sup>™</sup> meets Titanium with higher power density at 300kHz
- CoolMOS does not meet Titanium at 300kHz F<sub>sw</sub> in LLC stage
- GaNSafe<sup>™</sup> meets EN55022 / CISPR22 Class A (CE and RE)

#### GàNSafe<sup>™</sup> Yields Higher Density, Lower BOM **Navitas**

Si 150 kHz LLC

GàNSafe™

300 kHz LLC

Main Transformer		<b>PQ3628</b> Size: 36 x 28 x 24 mm		<b>PQ3033 = 14% smaller</b> Size: 30 x 33 x 21 mm
Resonant Choke		<b>PQ2618:</b> Size: 26 x 18 x 19 mm		<b>PQ2018 = 43% smaller</b> Size: 20 x 18 x 14 mm
Resonant Capacitor & Output MLCC		630 V, 10 nF 1206 *40pcs 16 V, 10 uF 1206 *64pcs		630 V, 10 nF 1206 *12pcs 16 V, 10 uF 1206 *50pcs = 40% fewer
Primary Switch	G Inflineng	IPT60R055CFD7 *4pcs	Novitas GaNSafe Power IC	NV6512C *4pcs
Protections & Features		1 kV ESD	A BARGA GANGAC Journ K And Canada	SCP, 2 kV ESD, dV/dt Control

# **GaNSafe**<sup>m</sup> Delivers Highest OBC Power Density Navitas



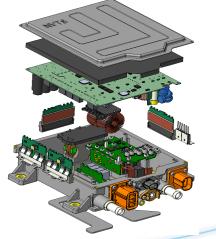
#### Combination 6.6 kW OBC + 3 kW DC-DC:

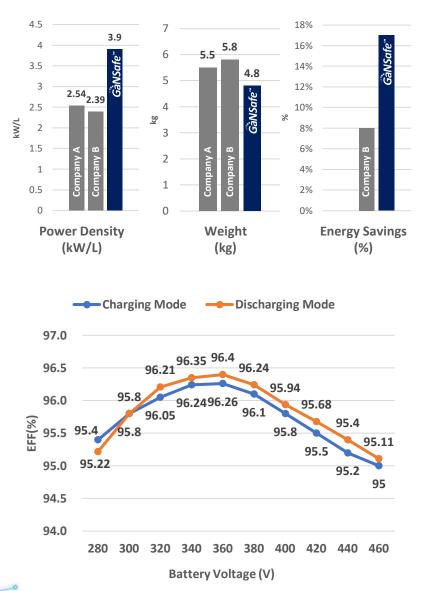
- AC Input: 90~265 V<sub>AC</sub> up to 32 A
- DC Output: 470~860  $V_{DC}$ , full load
- Power Output: 6.6 kW charging, 6.0 kVA discharging
- Efficiency: > 95% @ Full Load
- **DC-DC Output:**  $9^{-16} V_{DC}$

#### Mechanical:

- **Dimensions:** 210 x 192 x 61mm ( ≤ 2.5 litre)
- **Cooling:** -40 to +65°C (Cold Plate)
- Communication: IP 67, CAN Bus interface



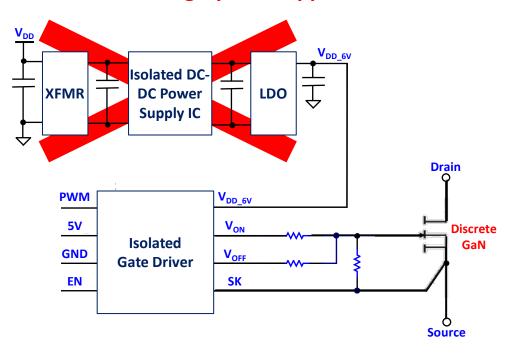




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# **Gansafe**<sup>TM</sup>: Lower BOM Cost than Discrete GaN Navitas

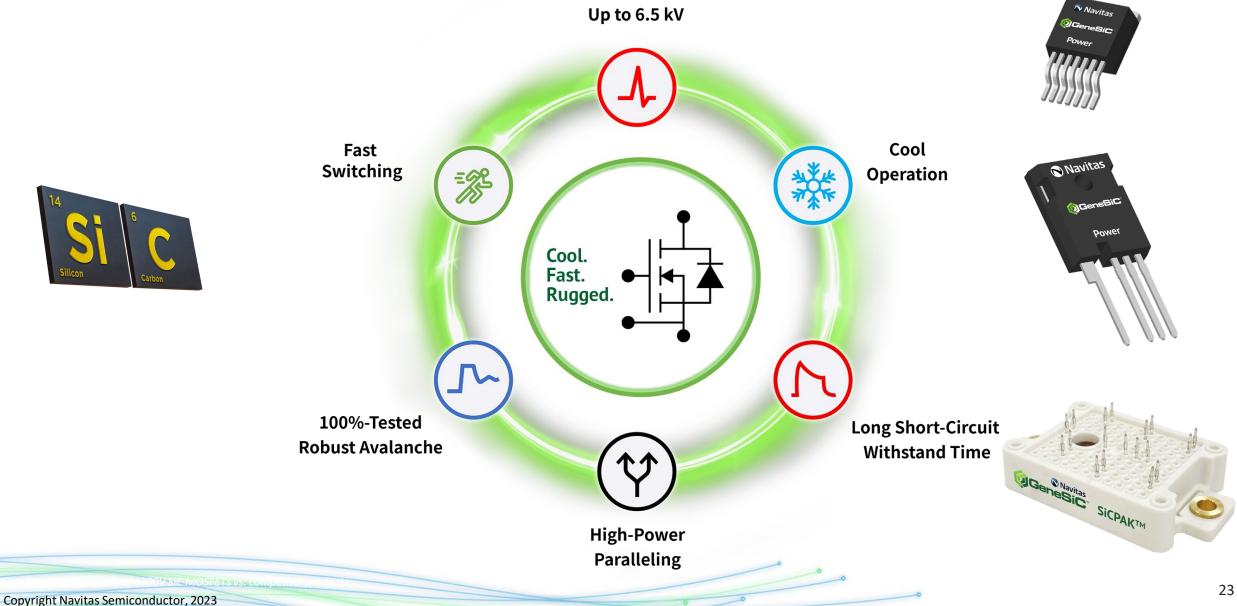
#### Typical GaN discrete high-side schematic for use in high-power applications:



#### **Eliminate costly DC-DC supply**

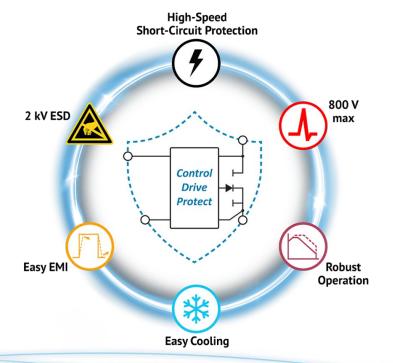
- *G*aNSafe™
  - Use Boot Strap for high power applications:
    \$1 saved per half-bridge
  - Integrated UVLO and Miller Clamp: no power sequencing issue, no negative gate drive (V<sub>GS</sub>)
  - Includes SCP protection and 2 kV ESD

### 😥 GeneSiC": Highest Performance & Ruggedness 🔊 Navitas



# **GaNSafe**<sup>TM</sup>: World's Safest GaN Power Semiconductor





- Navitas is the industry leader in GaN
- GanFast™:
  - 100M+ shipped, 20-year warranty



- GaNSafe<sup>™</sup> :
  - Overcomes challenges inherent to Discrete GaN
  - Most protected, most reliable, safest GaN power semiconductor in 4-Pin TOLL
  - Allows Benchmark efficiency, power density, reliability
  - Demonstrated performance for AI, EV, Solar and more
  - Driving GaN into high power

# Discover more at navitassemi.com

# **Navitas** Electrify Our World™



Navitas GàNSafe<sup>™</sup> Power IC