

SiC and GaN Applications in Electric Vehicles:
Current Issues

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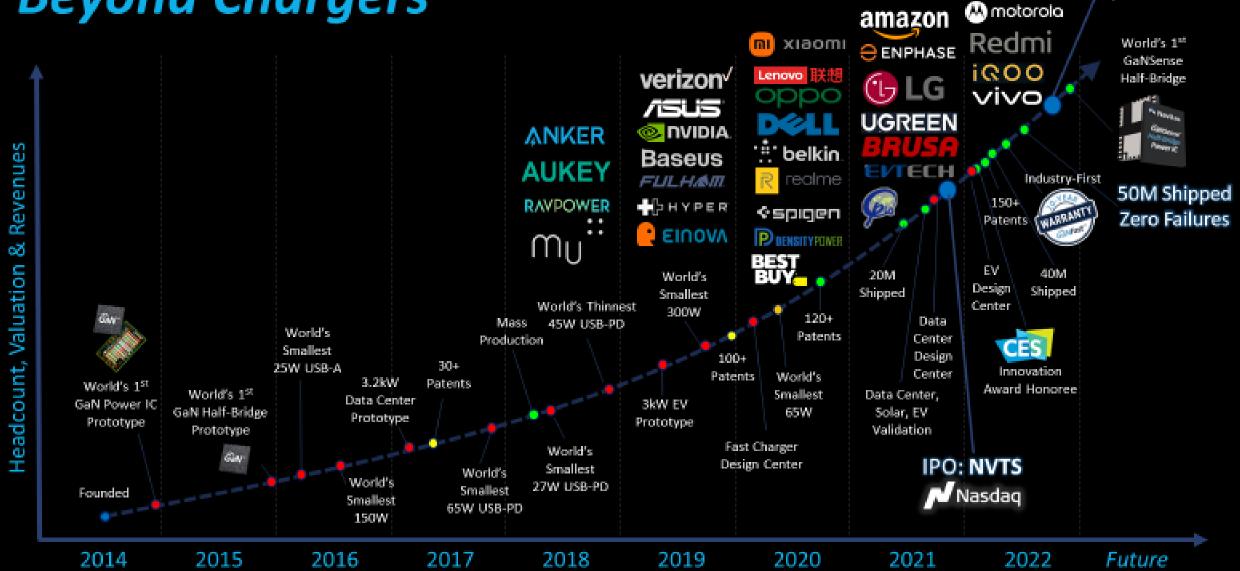
Accelerating Market adoption of WBG



- Customer understanding how to get the full benefit from using WBG
- Reliability and quality
- Offering the best WBG technology for the solution
- Solving supply chain issues
- Cost roadmap
- Environmental CO2 impact / ESG

#1 in Power GaN & Accelerating Growth

Beyond Chargers



(i)GeneSiC

SAMSUNG

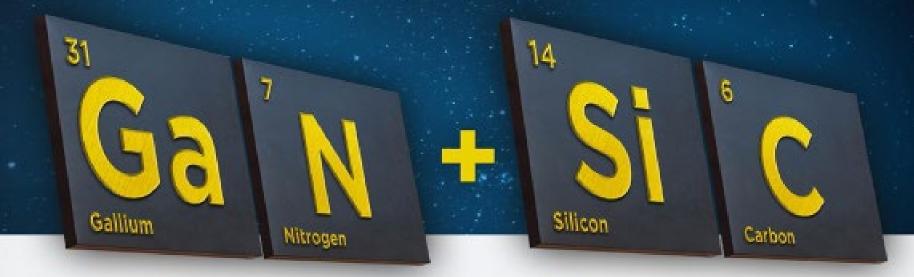
O Navitas Semiconductor 2022



Navitas Opens World's First Gal IC Design Center Dedicated to Electric Vehicles (EV)

Navitas Opens New Design Center Focused on Enabling GaN-based Data-Centers





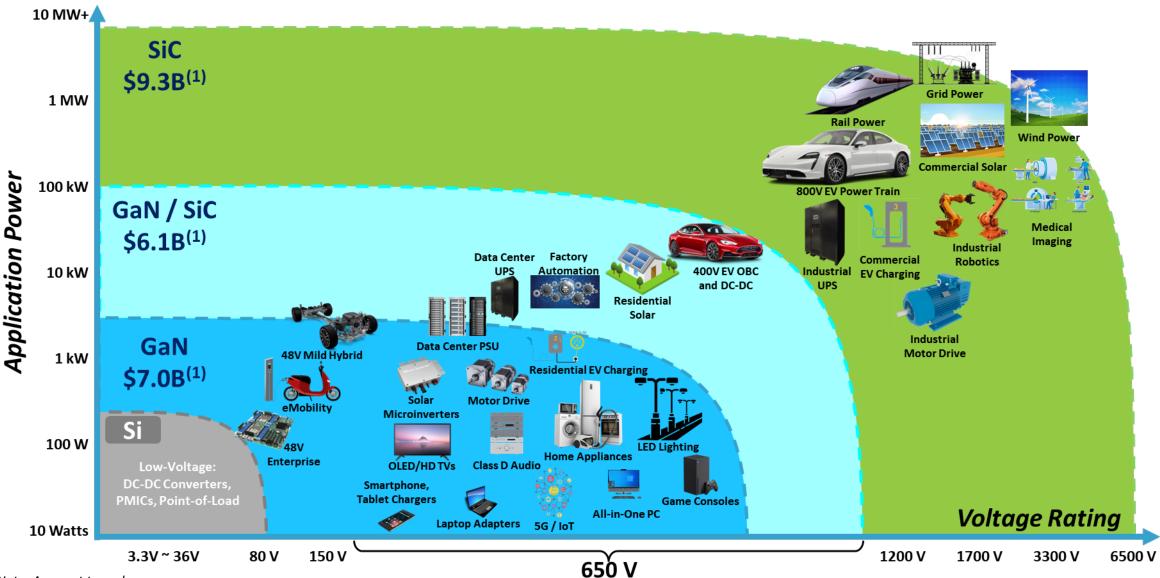




Pure-Play Next-Gen Power Semiconductors

Only Pure-Play Next-Gen Power Semi Company Navitas



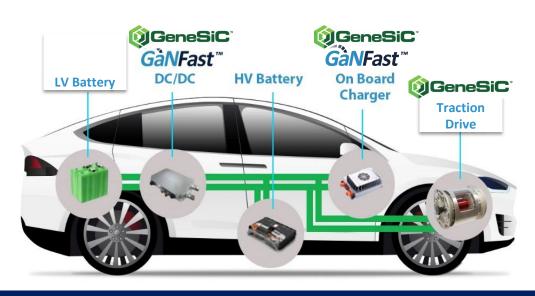


Note: Axes not to scale

Note (1): 2026E potential, Source: Yole, DNV, IRENA, Fraunhofer ISE, IHS, Cisco, Hyperscale, Peer annual reports, Wall Street research.

Pure-Play EV: The Largest Opportunity





\$12B/yr Potential for GaN/SiC by 2030⁽¹⁾

- ✓ OBC > \$38
- ✓ DC-DC > \$12
- ✓ Traction drive >\$286
- Total: >\$330 per EV = \$10.1B...and >\$1.9B in road-side chargers

Dedicated EV System Design Center

• 3 platforms

- 400V 6.6 kW W bi-directional charger (2-in-1)
- 800V 6.6 kW bi-directional charger and DC-DC (3-in-1)
- 22 kW wall charger to 400V, 800V

Synergistic & Engaged Customers











INOVANCE





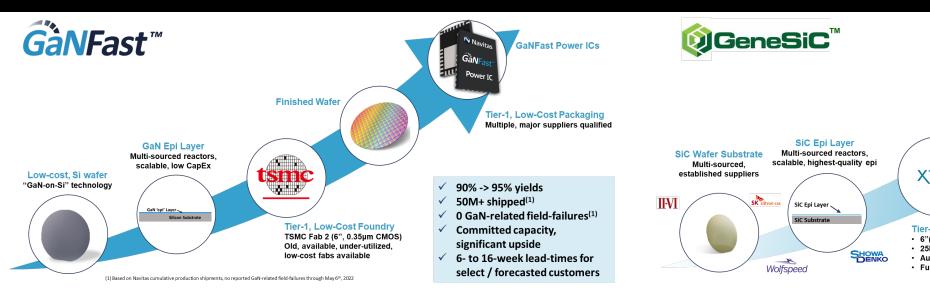


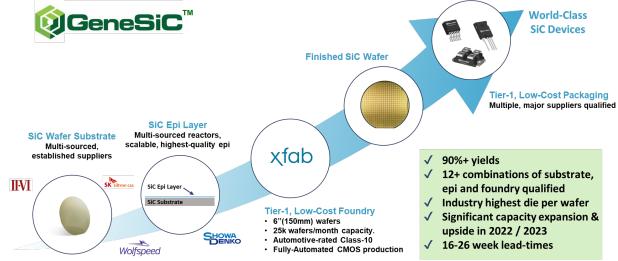




High Capacity, 60% Shorter Lead-times









20

30

40

Lead-time (weeks)

10

Navitas ~60% shorter lead-time

- Immediate availability for 1k prototyping
- GaNFast: Power ICs at 6-16 weeks
- GeneSiC: Rectifiers 16 weeks

FETs 16 weeks

GaN & SiC Manufacturing Costs



Manufacturing & Materials Cost Structures

	Substrate	Epi	Wafer Fab	Total Cost
GaN	Silicon very low cost many suppliers	GaN moderate cost growing suppliers	Silicon Fab low cost standard eqiup't	1x → 0.6x future
SiC	SiC high cost many suppliers	SiC moderate cost many suppliers	Silicon Fab low / moderate cost some non-std equip't	1.7x → 1.0x future

All above are relative to today's GaN wafer fab costs (ie, 1x). Relative costs are Navitas best estimates across the industry.

SiC Substrate Mfg Cost & Suppliers



- GaN has inherent manufacturing cost advantage utilizing Si substrates (vs SiC substrates)
- GaN and SiC epi costs are similar and reducing
- Wafer fab processing costs can be low when utilizing older, retrofit Si fabs
- SiC substrate cost structures and supplier options are improving dramatically

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Leader in Sustainability





February '22 First GaN sustainability report based on global standards.







May '22 World's first semiconductor company CarbonNeutral® certified



August '22 First 100,000 tons CO₂ saved

4x-10x lower component CO₂ footprint than silicon⁽¹⁾

28% lower lifetime CO₂ footprint for chargers / adapters⁽²⁾

Accelerates transition from ICE to EV by 3 years, saving 20%/yr of road-sector emissions by 2050 (4)

GaN saves up to 2.6 Gton / year by $2050^{(5)}$



October '22 Recognized for industry-leading sustainability reporting

Thank You Let's go ĜàNFast™

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