GeneSiC^{*}

G3F25MT06L

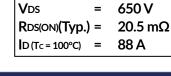
$650 V 20.5 m\Omega$ SiC MOSFET

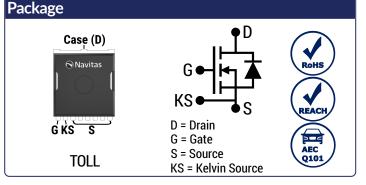
Silicon Carbide MOSFET

Trench-Assisted Planar Technology

Features

- Gen3F (3rd Generation) Technology
- Most Stable R_{DS(ON)} over Temperature
- Low Coss, Crss and Balanced Ciss/Crss
- Lower QGD and Balanced RG(INT)
- Electromagnetically Optimized Design
- Robust Body Diode with Low V_{F} and Low Q_{RR}
- 100% Avalanche (UIL) Tested
- AEC-Q101 Qualified





Advantages

- Superior Performance and Robustness
- Lowest Conduction Losses at all Temperatures
- Lesser Switching Spikes and Lower Losses
- Faster and More Efficient Switching
- Reduced Ringing
- Ease of Paralleling without Thermal Runaway
- Excellent Power Density and System Efficiency
- Enhanced System Reliability

Applications

- xEV OBC & DC-DC
- EV Fast Charging Infrastructure
- Solar / PV
- Energy Storage System
- Server & Telecom Power Supply
- Uninterruptible Power Supply
- Motor Control
- Class D Amplifiers

Absolute Maximum Ratings (At T_c = 25°C Unless Otherwise Stated)

Parameter	Symbol	Conditions	Values	Unit	Note
Drain-Source Voltage	V _{DS(max)}	V_{GS} = 0 V, I_D = 100 μ A	650	V	
Gate-Source Voltage (Dynamic)	V _{GS(max)}		-10/+22	V	
Gate-Source Voltage (Static)	V _{GS(op)} -ON	Recommended Operation	15 to 18	V	Note 1
	V _{GS(op)-OFF}		-5 to -3		
		T_{C} = 25°C, V_{GS} = -5 / +18 V	125		
Continuous Drain Current	I _D	T_{C} = 100°C, V_{GS} = -5 / +18 V	88	Α	Fig. 16
		T_{C} = 135°C, V_{GS} = -5 / +18 V	64		
Pulsed Drain Current	I _{D(pulse)}	$t_P \le 3\mu s$, $D \le 1\%$, V_{GS} = 18 V	175	А	Note 2
Power Dissipation	PD	T _c = 25°C	455	W	Fig. 17
Non-Repetitive Avalanche Energy	Eas	L = 36 mH, I _{AV} = 5 A	540	mJ	
Operating Junction and Storage Temperature	T _j , T _{stg}		-55 to 175	°C	,

Note 1: This product can support 0V turn-off gate drive voltage with optimized PCB layout and gate drive circuit configuration.

Note 2: Pulse Width tP Limited by Tj(max)

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Electrical Characteristics (At	T _C = 25°C Unle	ess Otherwise Stated)						
Parameter	Symbol		Values					
		Conditions	Min.	Тур.	Max.	Unit	Note	
Drain-Source Breakdown Voltage	V _{DSS}	V_{GS} = 0 V, I _D = 100 µA	650			٧		
Zero Gate Voltage Drain Current	IDSS	V_{DS} = 650 V, V_{GS} = 0 V	1 100		μA			
Orthe Orthographic state of the second state	I _{GSS}	$V_{DS} = 0 V$, $V_{GS} = 22 V$			100	n۸		
Gate Source Leakage Current		V_{DS} = 0 V, V_{GS} = -10 V	-1		-100	nA		
Gate Threshold Voltage	V _{GS(th)}	V_{DS} = V_{GS} , I_D = 15 mA	2.2	2.8	4.3	۷	Note 3	
Transconductance	04	V_{DS} = 10 V, I_{D} = 35 A	19.2 19.3		S	Fig. 5		
	G fs	V _{DS} = 10 V, I _D = 35 A, T _j = 175°C			3			
		V_{GS} = 18 V, I_D = 35 A		20.5	27.5			
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} = 18 V, I _D = 35 A, T _j = 175°C	27		mΩ	Fig. 5-9		
Drain Source on State Resistance	TDS(UN)	V_{GS} = 15 V, I_D = 35 A		29				
		V _{GS} = 15 V, I _D = 35 A, T _j = 175°C		32				
Input Capacitance	Ciss			2939				
Output Capacitance	Coss		212			pF	Fig. 12	
Reverse Transfer Capacitance	Crss			12.4				
Coss Stored Energy	Eoss	V _{DS} = 400 V, V _{GS} = 0 V		19		μJ	Fig. 13	
Coss Stored Charge	Q _{oss}	$f = 500 \text{ KHz}, V_{AC} = 25 \text{mV}$		134		nC		
Effective Output Capacitance (Energy Related)	C _{o(er)}			238		- 5	Note 4	
Effective Output Capacitance (Time Related)	C _{o(tr)}		335		рF	Note 4		
Gate-Source Charge	Q _{gs}	V _{DS} = 400 V, V _{GS} = -5 / +18 V		26				
Gate-Drain Charge	Q _{gd}	$I_D = 35 \text{ A}$		31		nC	Fig. 11	
Total Gate Charge	Qg	Per JEDEC JEP-192		108				
Internal Gate Resistance	R _{G(int)}	V _{GS} = 18 V, f = 1 MHz, V _{AC} = 25 mV	1.3		Ω			
Turn-On Switching Energy (Body Diode)	E _{On}	T _i = 25°C, V _{GS} = -5/+18V, R _{G(ext)} = 2.2 Ω, L	44		μJ	Fig. 24-27		
Turn-Off Switching Energy (Body Diode)	Eoff	= 80.0 μH, I _D = 35 A, V _{DD} = 400 V	24					
Turn-On Delay Time	t _{d(on)}		23 8 16					
Rise Time	tr	$V_{DD} = 400 \text{ V}, \text{ V}_{GS} = -5/+18 \text{ V}$			-	F ' 66		
Turn-Off Delay Time	t _{d(off)}	- R _{G(ext)} = 2.2 Ω, L = 80.0 µH, I _D = 35 A $-Timing relative to Vex. Industive lead$			ns	Fig. 26		
Fall Time	tf	 Timing relative to V_{DS}, Inductive load 		7		-		

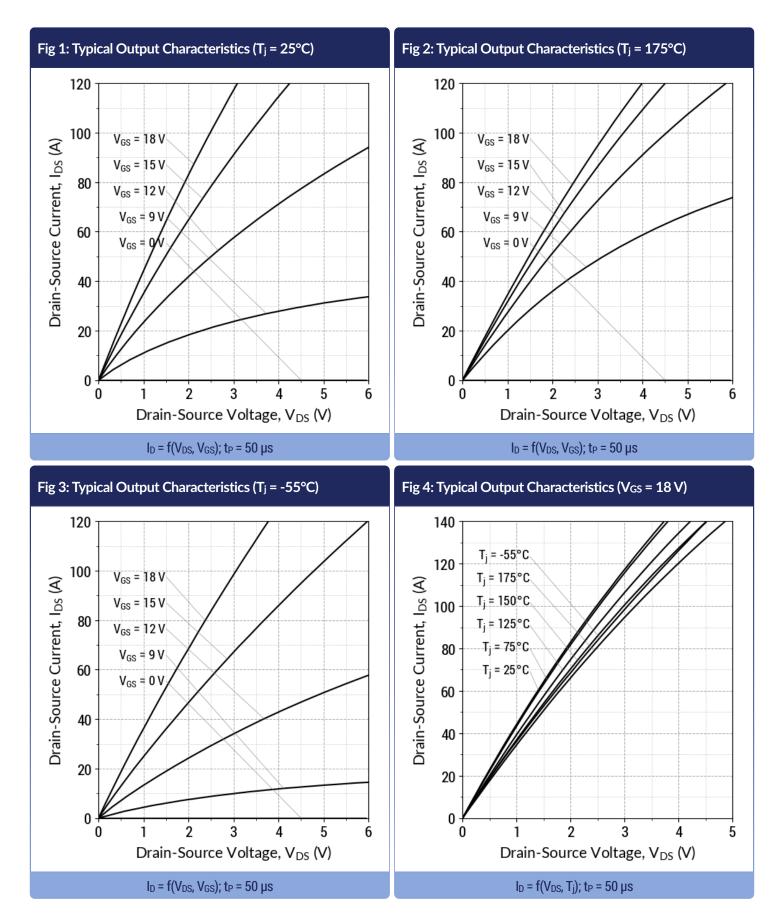
Note 3: Tested after applying 30ms pulse at Vgs= +25V

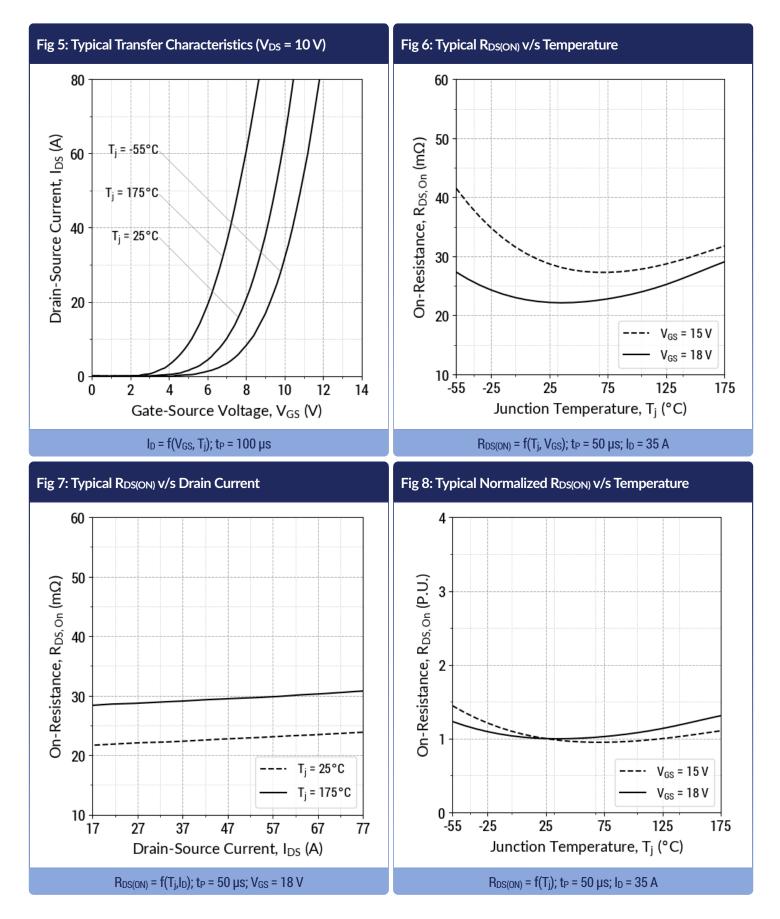
Note 4: $C_{o(er)}$, a lumped capacitance that gives same stored energy as C_{OSS} while V_{DS} is rising from 0 to 400V. $C_{o(tr)}$, a lumped capacitance that gives same charging times as C_{OSS} while V_{DS} is rising from 0 to 400V.

Reverse Diode Characteristics

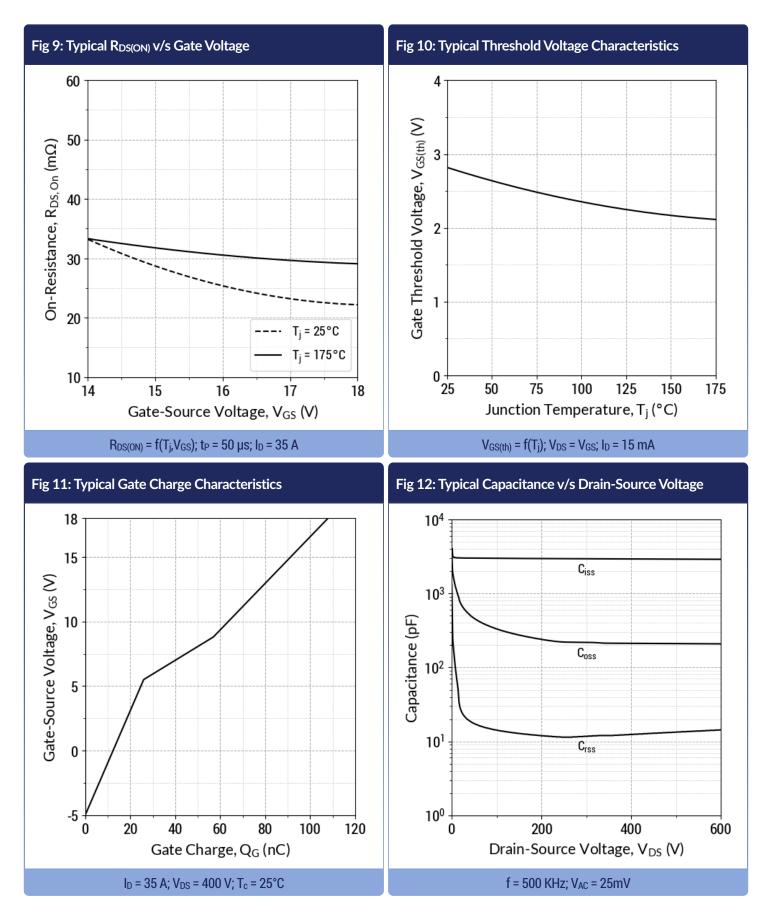
Parameter	Symbol	Conditions		Values		Unit	Nete
Farameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	Note
Diode Forward Voltage	N/	V_{GS} = -5 V, I_{SD} = 17 A		4.4		V	Fig. 10.10
	V _{SD}	V _{GS} = -5 V, I _{SD} = 17 A, T _j = 175°C		3.9		v	Fig. 18-19
Continuous Diode Forward Current	I-	V _{GS} = -5 V, T _c = 25°C	68		٨		
	ls	V _{GS} = -5 V, T _c = 100°C			41	A	
Diode Pulse Current	I _{S(pulse)}	V _{GS} = -5 V		164		Α	Note 2
Reverse Recovery Time	t _{rr}			16		ns	
Reverse Recovery Charge	Qrr	V _{GS} = -5 V, I _{SD} = 35 A, V _R = 400 V dif/dt = 2400 A/µs, T _i = 25°C		165		nC	
Peak Reverse Recovery Current	Irrm	$4770 - 2400 A/\mu s, 1j - 25 C$		34		Α	
Reverse Recovery Time	t _{rr}			20		ns	
Reverse Recovery Charge	Qrr	V _{GS} = -5 V, I _{SD} = 35 A, V _R = 400 V dif/dt = 2400 A/µs, T _i = 175°C		320		nC	
Peak Reverse Recovery Current	Irrm	un/ut - 2400 Α/μs, 1 _j - 175 C		47		Α	

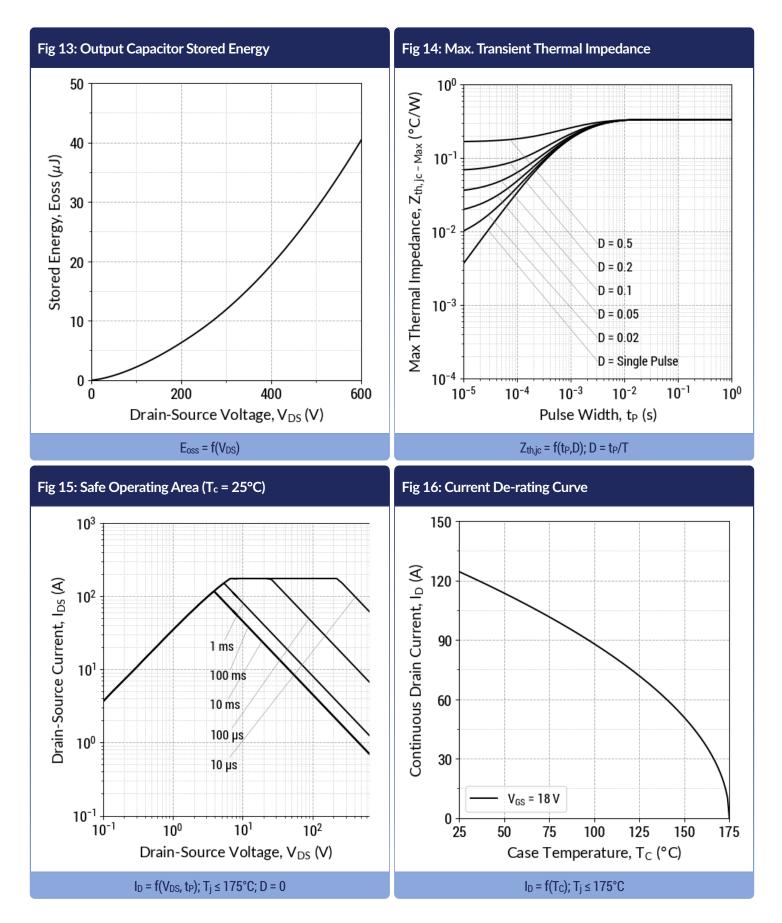
Package Characteristics					
Parameter	Symbol	Conditions	Values	Unit	Note
Max Thermal Resistance, Junction - Case	R _{thJC-Max}	Maximum	0.33	°C/W	Fig. 14
Weight	WT		1.2	g	
Moisture Sensitivity Level	MSL		1		
EMC Material Group			II		



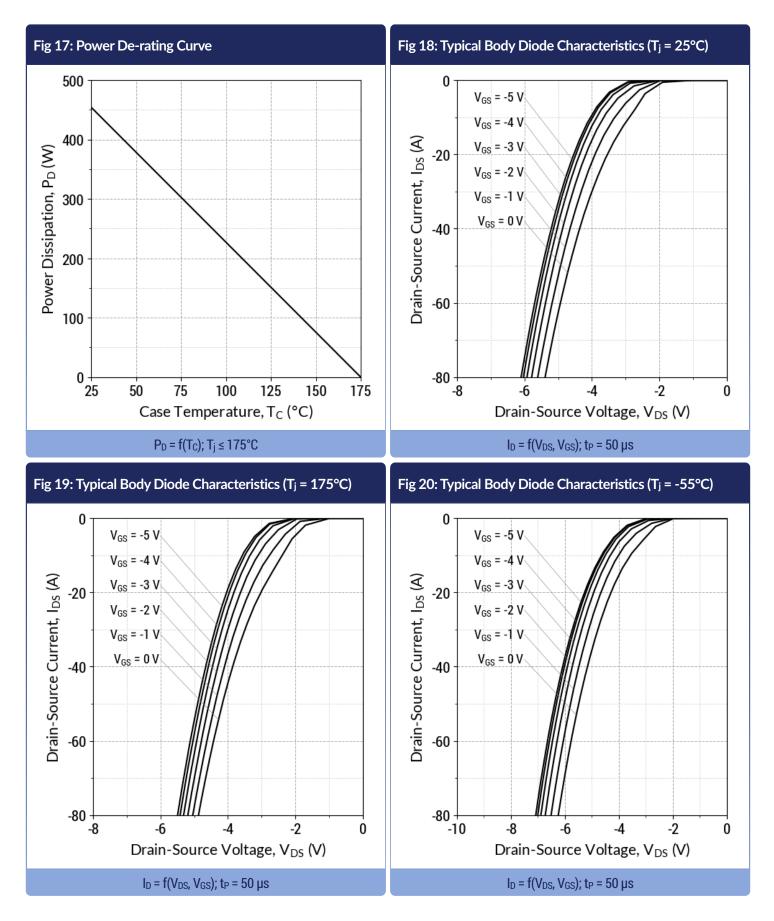


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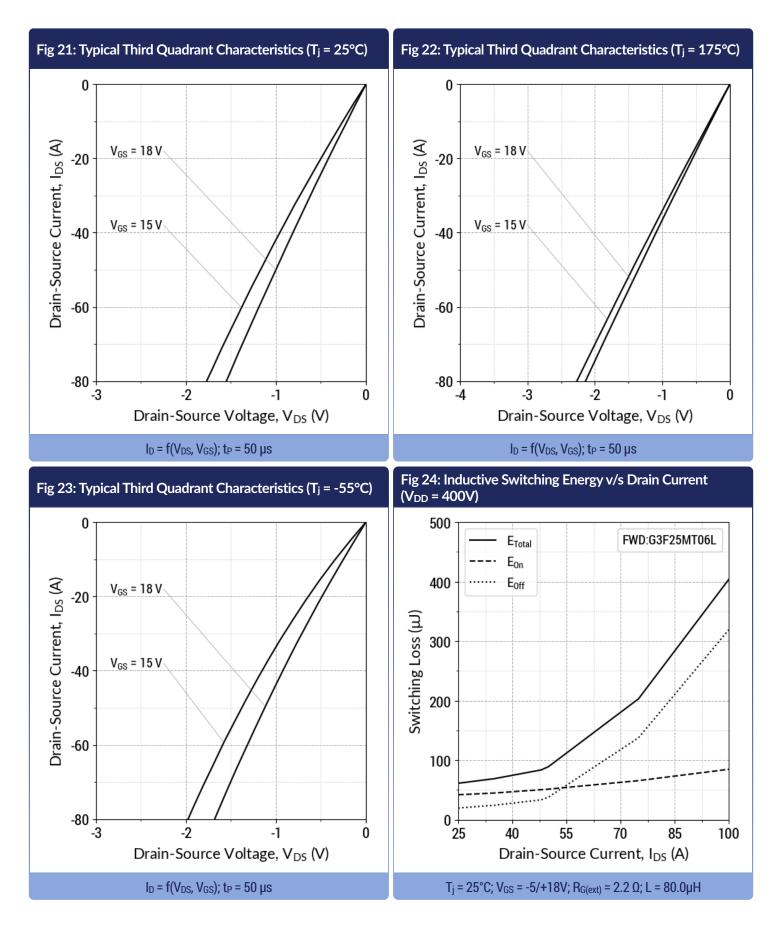




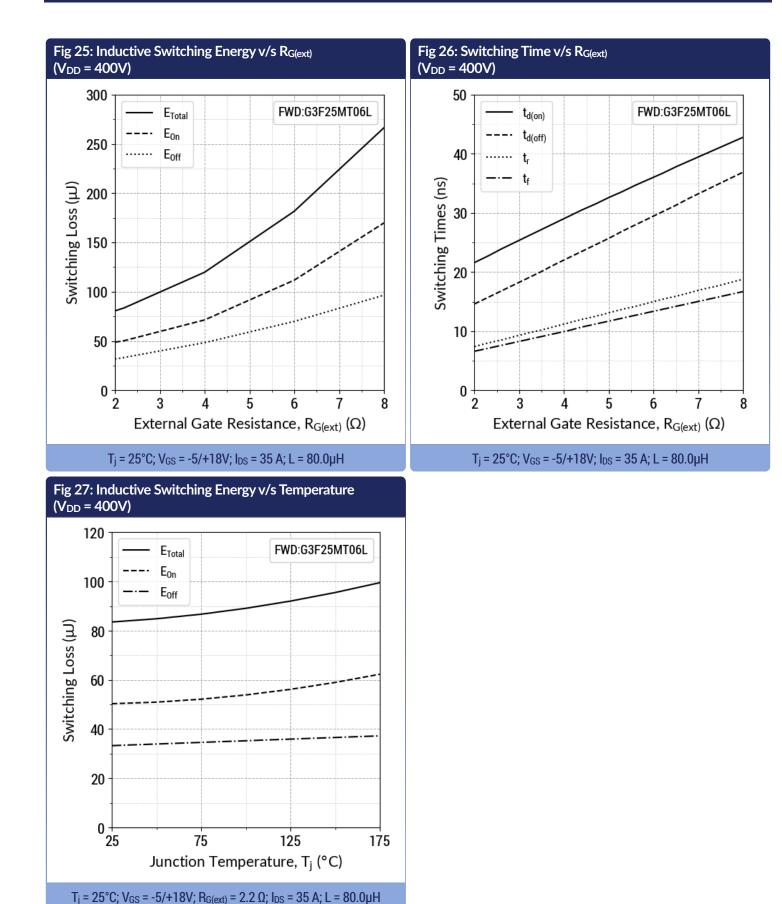
GeneSiC^{*}



GeneSiC

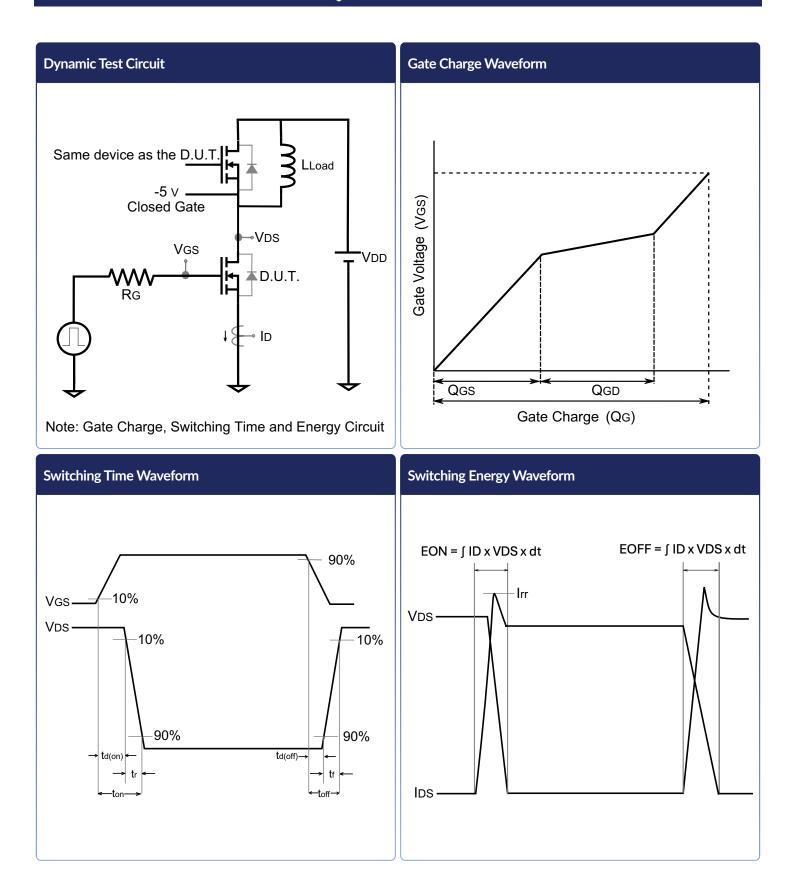


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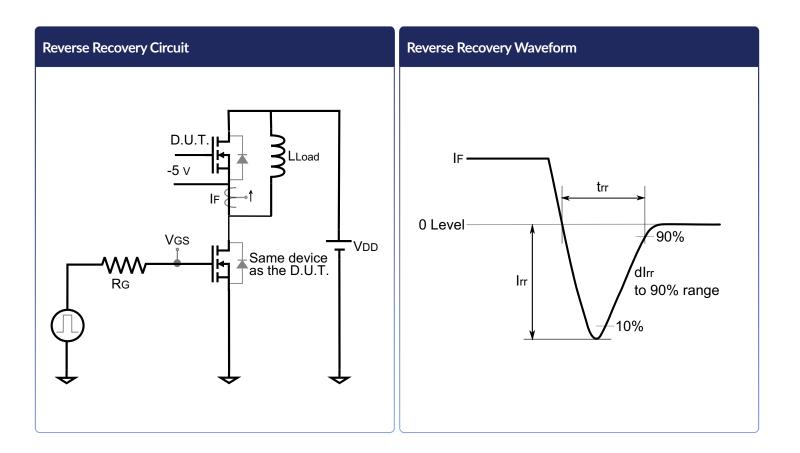


Navitas

GeneSiC

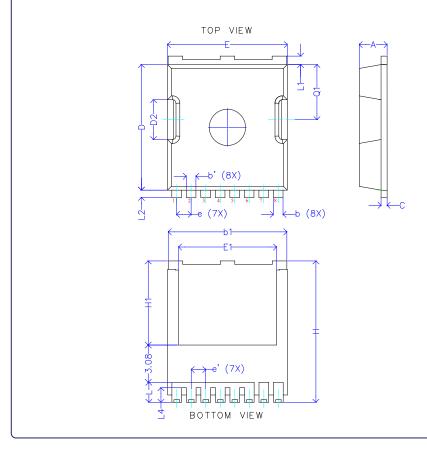


ØGeneSiC^{*}



Package Dimensions

TOLL Package Outline



SYMBOL	COMMON	DIMENSIO	NS (MM)
	MIN	NOM	MAX
A	2.15	2.30	2.45
b	0.70	0.75	0.85
b'	0.65	0.70	0.80
b1	9.65	9.80	9.95
С	0.45	0.50	0.60
D	10.18	10.38	10. 58
D2	3.15	3.30	3.45
Е	9.70	9.90	10.10
E1	7.95	8.10	8.25

AM)
AX
70
. 88
10
00
90
72
30

NOTE

1. CONTROLLED DIMENSION IS MILLIMETER.

2. DIMENSIONS DO NOT INCLUDE END FLASH, MOLD FLASH, MATERIAL PROTRUSIONS.

Revision History

Rev 24/Aug: Initial Release (Rev 1.0)

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