



## Layout Engineer

*Location: Shanghai*

### **Role & Responsibilities:**

- Provide accurate schedule estimations to given layout tasks
- Contribute to hierarchical planning (top down and bottom up) and integration
- Communicate with design engineers to negotiate any necessary layout trade-offs as needed to build complex analog layout
- Collaborate with off-site/on-site layout engineer
- Deliver high quality layout that meets all design requirements.

### **Requirements:**

- BS. with 8+ years experience in analog/mixed-signal layout design.
- Experience in power management IC and chip level layout is highly preferred.
- Experience in high voltage device layout is preferred
- High-level proficiency in EDA tools such as Cadence/Mentor Graphics LVS/DRC/ERC/Antenna check tools
- Deep understanding of Well isolation, EM rule, antenna effect, matching etc.
- Experience in building analog blocks, high voltage devices, pad IOs, ESD structures, etc.
- Experience in version control software such as cliosoft or designSync, etc.
- Programming knowledge in cadence SKILL language, Perl, and/or Python is a bonus, but not required
- Excellent communication skills and able to work with multi-functional teams.
- Self-motivated, positive work attitude with a desire to work in a startup environment.