



Navitas Semiconductor, Inc.

Job Description:

Title: Principal Design Engineer (Principal Analog IC Designer)

Reports to: VP of IC Design

In Office /Remote: /Hybrid

Exempt / Non-exempt

Based: Home Based

Job Purpose:

Navitas Semiconductor (Nasdaq: NVTS) is a high-growth, publicly traded technology company seeking a Principal Design Engineer. The ideal candidate will be self-motivated, energetic, tech-savvy, collaborative, and understands the dynamics of a fast-growing company.

Key Responsibilities and Duties:

- Evaluate system-level trade-offs for next generation power supply and identify IC architectures in collaboration with the system application team to meet performance requirements
- Interface with characterization, modeling, and EDA design engineers to define and optimize device characteristics and simulation needs
- Guide layout engineers on physical design
- Review layouts and floorplan and provide feedback for optimal layout design
- Tape out designs on schedule, adhering to required specifications
- Evaluate and validate designs in the lab and identify any deviations from requirements and implement corrective actions
- Any and all other duties, as assigned

KSAOs:

- Knowledge and strong understanding of semiconductor device physics, device safe-operating area, and power transistors used for high-frequency switching
- Proven track record of designing power IC from concept phase to massive production
- Strong proficiency with EDA tools (Cadence Specter, Virtuoso XL, Post Parasitic Extraction simulations, Monte Carlo Analysis, etc.)
- Experience with DFT approaches and development of characterization and production test plans
- Professionalism towards internal and external partners
- Demonstrated ability to autonomously manage a myriad of demands and prioritize effectively
- Excellent written and verbal communication skills
- Maintains accountability for actions (ownership of work)
- Extremely reliable and self-motivated with the ability to champion tasks

Requirements:

Basic:

- Master's degree in electrical engineering
- 10+ years of IC Design experience

Preferred:

- PhD in electrical engineering
- 12+ years of IC Design experience



- Extensive previous experience in chip design in the power supply field (Analog IC Design, Power Management IC, PMIC, AC-DC, AC-DC Controller, ACF, LLC, QR)

Position Qualifies for the Following Compensation

Base Salary: \$139,684.60 - \$200,853.67

Equity Compensation (RSUs)

Personal Performance Bonus

Company Performance Bonus