Navitas Semiconductor

Job Description:

Title: **Staff/Sr. Staff/Principal IC Layout Engineer**

Reports to: VP, China IC Design

In Office ☒/Remote ☐/Hybrid☐

Based: Shanghai/Hangzhou/Shenzhen

Job Purpose:
Navitas Semiconductor (Nasdaq: NVTS) is a high-growth, publicly traded technology company seeking a Staff/Sr. Staff/Principal IC Layout Engineer. The ideal candidate will be self-motivated, energetic, tech-savvy, collaborative, and understands the dynamics of a fast-growing company.

Key Responsibilities and Duties:

- Provide accurate schedule estimations to given layout tasks
- Contribute to hierarchical planning (top down and bottom up) and integration
- Communicate with design engineers to negotiate any necessary layout trade-offs as needed to build complex analog layout
- Collaborate with off-site/on-site layout engineer
- Deliver high quality layout that meets all design requirements.

Requirements:

- BS. with 8+ years experience in analog layout design.
- Experience in power management IC and chip level layout is highly preferred.
- Experience in high voltage device layout is preferred
- High-level proficiency in EDA tools such as Cadence/Mentor Graphics LVS/DRC/ERC/Antenna check tools
- Deep understanding of Well isolation, EM rule, antenna effect, matching etc.
- Experience in building analog blocks, high voltage devices, pad IOs, ESD structures, etc.
- Experience in version control software such as cliosoft or designSync, etc.
- Programming knowledge in cadence SKILL language, Perl, and/or Python is a bonus, but not required
- Excellent communication skills and able to work with multi-functional teams.
- Self-motivated, positive work attitude with a desire to work in a startup environment.

纳微半导体

职位介绍：

职位: 主任/高级主任/首席 芯片版图设计工程师

汇报线: 中国芯片设计副总裁

In Office ☒/Remote ☐/Hybrid☐

地点: 上海/杭州/深圳

岗位职责：

- 负责各种电源芯片架构以及从底层模块到顶层电路的设计、仿真和验证
- 指导版图工程师完成版图设计和验收
- 协助测试工程师进行芯片的调试和测试
- 芯片 debug 和各种失效分析

任职要求：

- 硕士及以上学历，5 年以上模拟或者混合信号芯片设计经验
- 有成功量产经历，完成过从芯片架构、模块设计、顶层仿真、芯片后仿真、流片测试、各种失效和良率分析等全部流程
- 熟练掌握各种 EDA 工具使用
- 熟悉器件物理和工艺
- 熟练掌握各种模拟基本单元电路，例如 bandgap、LDO、运放、比较器，charge pump 等
- 有 AC-DC 或者 GaN 芯片设计背景更佳
- 良好自我驱动力、团队合作意识和沟通能力

https://www.navitassemi.com/